

Todo/Changes List

Rev p (previous)

- +30dB too high: gain set resistor 68R changed to 133R (10353853)
- When +11dBu in at 0dB gain is required, decrease gain: R115, 116 ... 10K-->15K

Rev A

(Migrated from version with RPI and ethernet stack. Updated rectifiers, added new +5V psu, added front leds, added front switches, added PSU to main pcb)

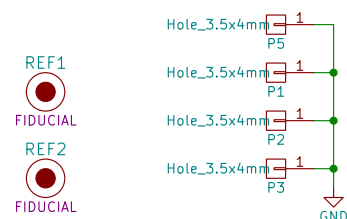
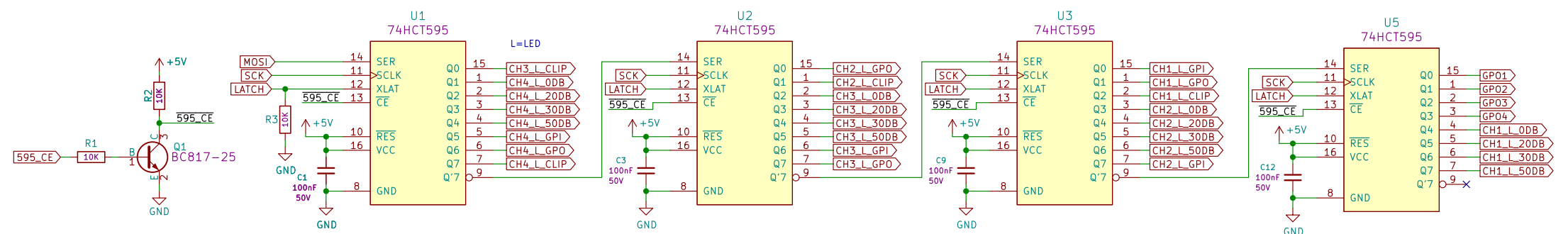
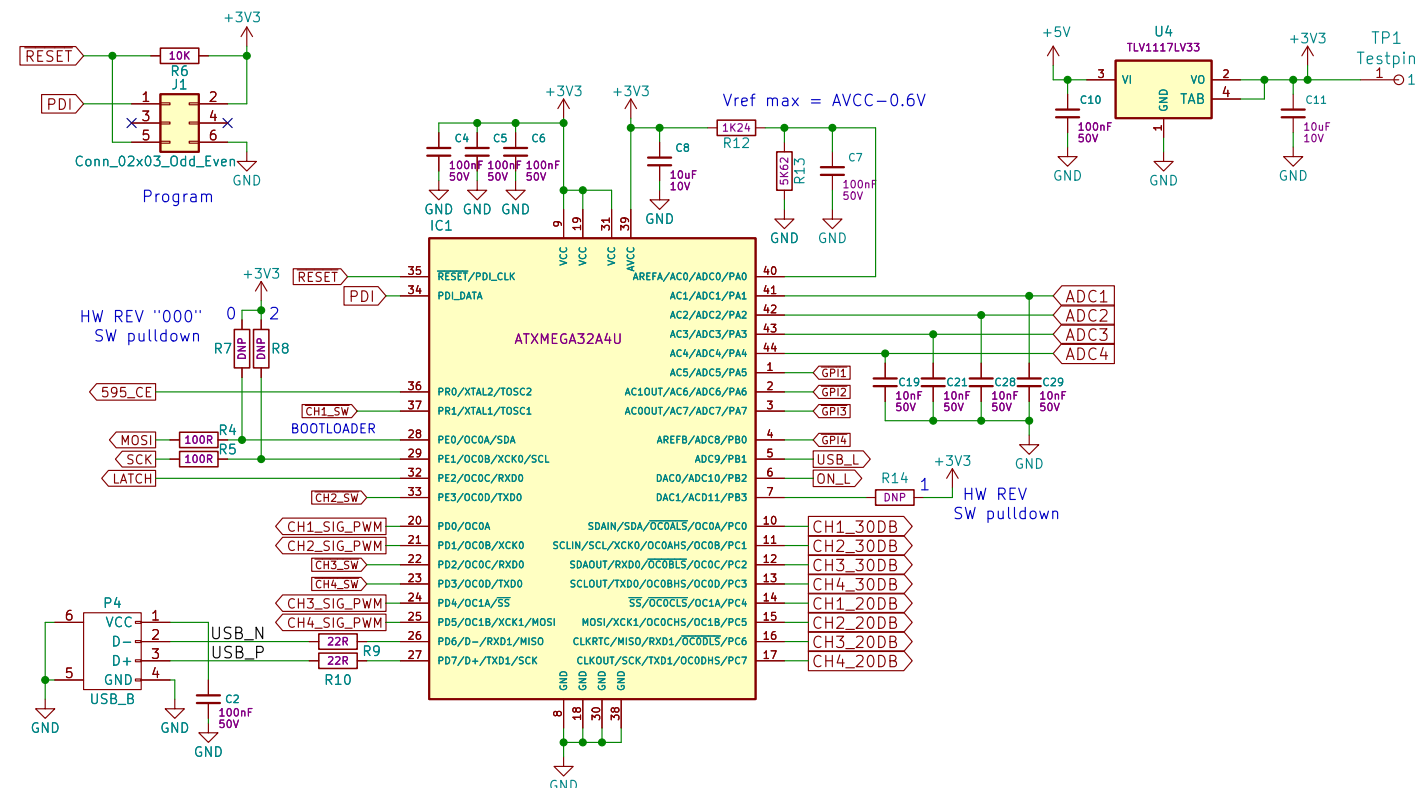
- Move USB connector in line with other connectors (now too far back)
- Make mounting holes oval -done/M
- Add protection diodes to rectifier outputs -done/M
- Make input coupling cap DNM if xformer != used (C41 etc) -done/M
- Make switch footprint THT holes tighter -done/M
- Switch from LRS-35-12 to IRM-5-5 due to 1khz hiccup -done/M
- Delete TPS 5V PSU -done/M
- Move switches 1.5mm more outwards -done/P
- Left down corner cutout -done /P

Limit bandwidth of micamps (for ch 1: C68, C672 $\rightarrow$ 820pF, R84 $\rightarrow$ 10k, R82 $\rightarrow$ 1k1) -done/M

- change leds from 0805 to 3528 to prevent bleeding (@5V blue and red = 2k, green = 470R, green @3v3 = 100R)
- Change +-15V to +-12V to limit current draw of t1072's

REV C:

- add USB protection diodes
- add better filtering on $\pm 12V$



Leds_GPI_GPO

File: Leds_GPI_GPO.kicad_sch

POWER

File: POWER.kicad_sch

Rectifiers

File: Rectifiers.kicad_sch

Micamps

File: Micamps.kicad_sch

10200002
WB/MH

D&R

Sheet:

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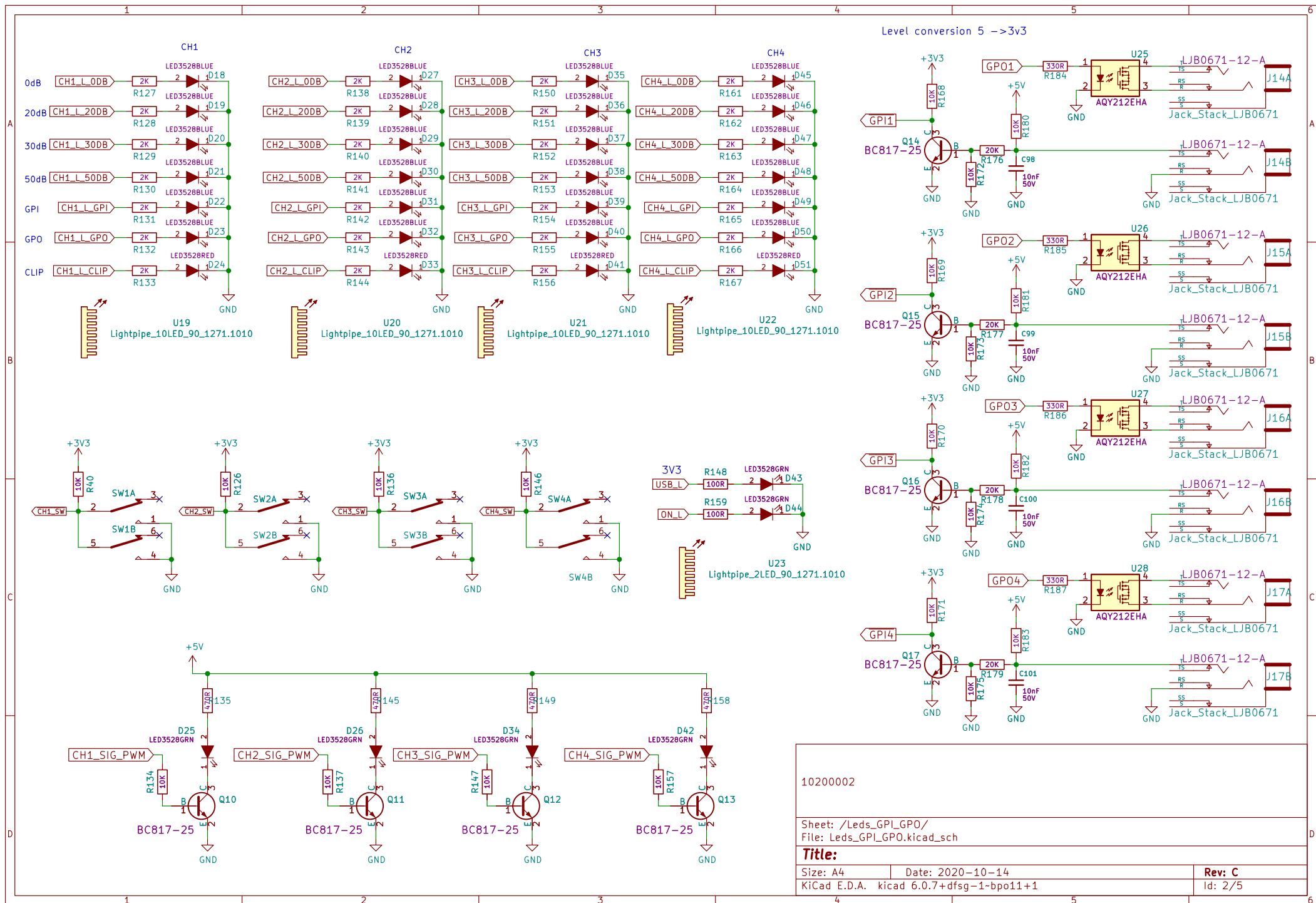
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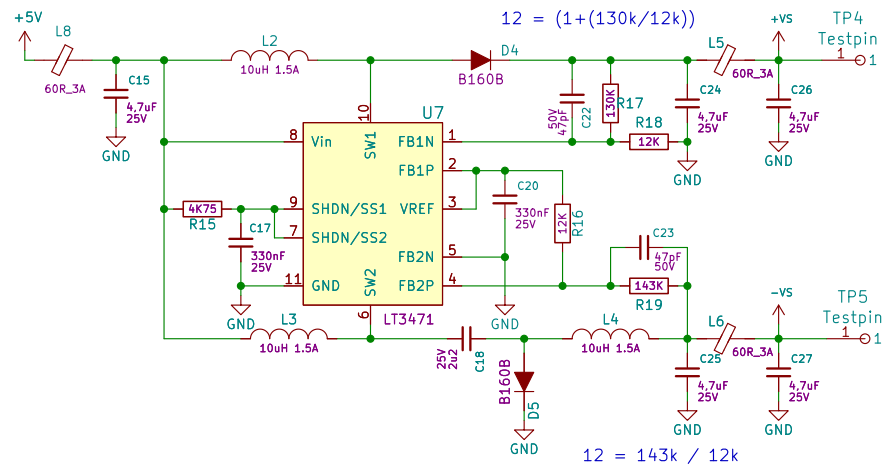
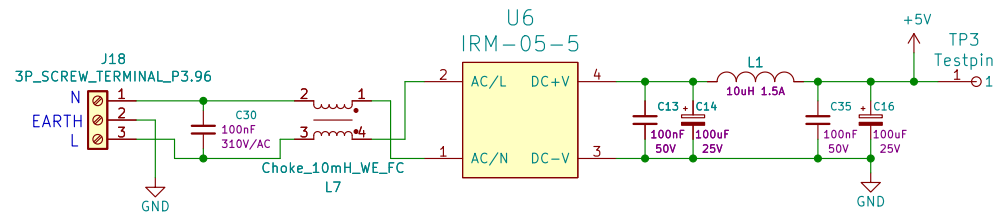
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Rev: C

Id: 1/5





10200002

WB

D&R

Sheet: /POWER/

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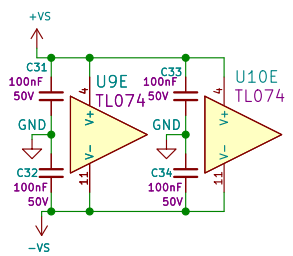
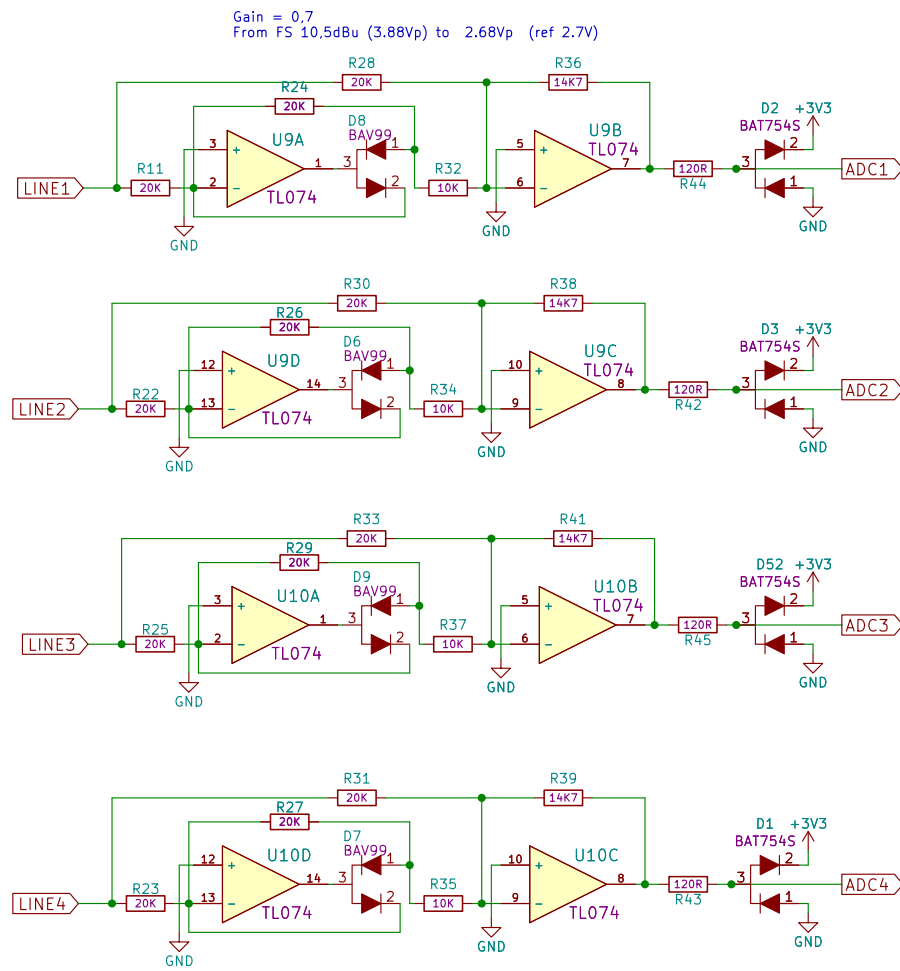
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Size: A4 Date: 2020-10-14

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Rev: C

Id: 3/5



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D&R	
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Size: A4	Date: 2020-10-14
KiCad E.D.A. kicad 6.0.7+dfsg-1-bpo11+1	Rev: C
	Id: 4/5

